TOSHIBA Photocoupler GaAs Ired & Photo-MOS FET

TLP206G

PBX

Modem · FAX Card

Measurement Instrument

The TOSHIBA TLP206G consists of gallium arsenide infrared emitting diode optically coupled to a photo-MOS FET in a 8 pin SOP. The TLP206G is a 2-Form-A switch which is suitable for replacement of mechanical relays in many application.

SOP 8 pin (2.54SOP8): 2-Form-A

Peak off-state voltage: 350V(min)

Trigger LED current: 3mA(max)

On-state current: 120mA(max)

• On-state resistance: 35Ω(max)

Isolation voltage: 1500V_{rms}(min)

UL recognized: UL1577, file no.E67349

BSI approved: BS EN60065: 2002, certificate no.8753

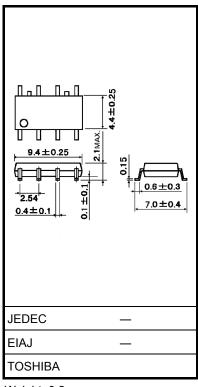
BS EN60950-1: 2002, certificate no.8754

SEMKO approved: SS EN60065

SS EN60950

Option(V4)type

TUV approved: DIN EN 60747-5-2, certificate No. 40009351 Unit in mm

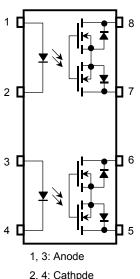


Weight: 0.2 g

Schematic

2-Form-A 1, 3 o

Pin Configuration (top view)



- 2, 4: Cathpde
- 5: Drain D1
- 6: Drain D2
- 7: Drain D3
- 8: Drain D4

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Absolute Maximum Ratings (Ta = 25°C)

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	Characte	ristic	Symbol	Rating	Unit	
	Forward current	l _F	50	mA		
	Forward current derating	ΔI _F / °C	-0.5	mA / °C		
LED	Pulse forward current (10	I _{FP}	1	Α		
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off-state output terminal	V _{OFF}	350	V		
	On–state current	Both channel (Note 1)		100	mA	
Detector		One channel	ION	120		
Dete	On–state RMS current derating(Ta ≥ 25°C)	Both channel (Note 1)		-1.0	mA / °C	
		One channel	ΔI _{ON} / °C	-1.2		
	Junction temperature	Tj	125	°C		
Stor	age temperature range		T _{stg}	−55~125	°C	
Оре	erating temperature range		T _{opr}	−40~85	°C	
Lea	d soldering temperature (1	0 s)	T _{sol} 260		°C	
Isola	ation voltage (AC, 1 min.,	R.H.≤ 60%) (Note 2)	BVS	1500	V _{rms}	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Two channels operating simultaneously.

(Note 2): Device considered a two–terminal device: Pins1,2,3 and 4 shorted together and pins 5,6,7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit	
Supply voltage	V_{DD}	_	_	280	V	
Forward current	lF	5	7.5	25	mA	
On-state current	I _{ON}	_	_	100	mA	
Operating temperature	T _{opr}	-20		65	°C	

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	-	pF
ctor	Off-state current	I _{OFF}	V _{OFF} = 350 V	_	_	1	μΑ
Detector	Capacitance	C _{OFF}	V = 0,f = 1MHZ	_	40	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Mln.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	I _{ON} = 120 mA	_	1	3	mA
On-state resistance	R _{ON}	I _{ON} = 120 mA, I _F = 5 mA	_	22	35	Ω

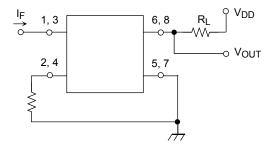
Isolation Characteristics (Ta = 25°C)

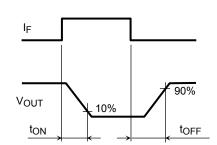
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	Cs	V _S = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	1500	_	_	V
Isolation voltage		AC, 1 second, in oil	_	3000	_	V _{rms}
		DC, 1 minute, in oil	_	3000	_	V _{dc}

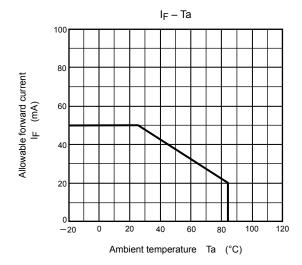
Switching Characteristics (Ta = 25°C)

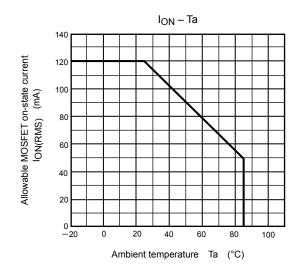
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t _{ON}	$R_L = 200\Omega$ (Note 3)	_	0.3	1	ms
Turn-off time	toff	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$	_	0.1	1	1113

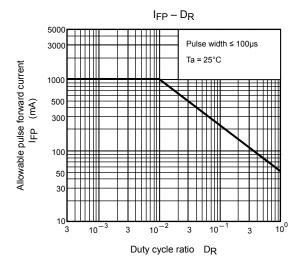
(Note 3): Switching time test circuit

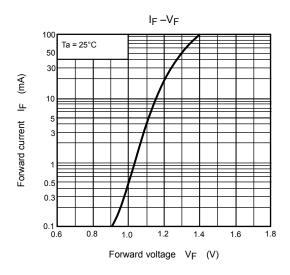


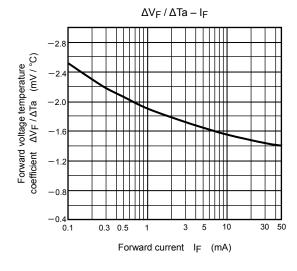


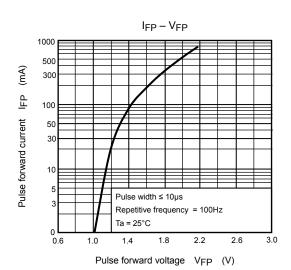


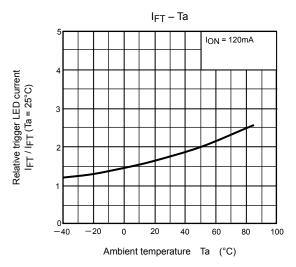


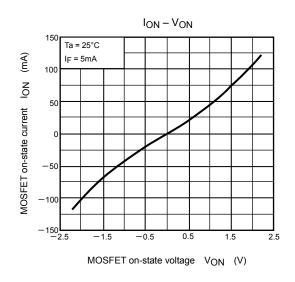


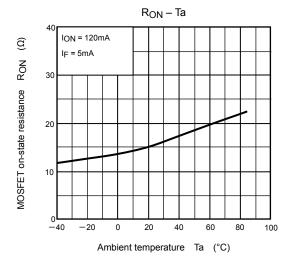


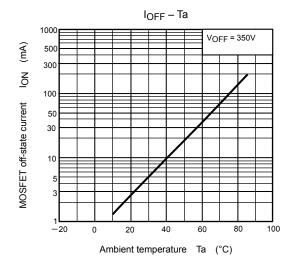


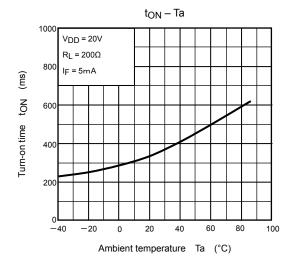


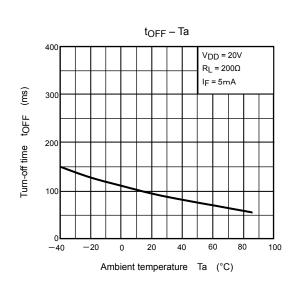












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